fadc	113–246									
an 12	b13									
an 11	b12									
an 10	b11									
an 9	b10									
an 8	b9									
an 7	b8									
an 6	b7									
an 5	b6									
an 4	b5									
an 3	b4									
an 2	b3									
an 1	b2									

tdc configuration

word	tdc1	tdc2	tdc3	tdc4	tdc5	tdc6	tdc7	tdc8	tdc9	tdc10
4	**PC4B** 9–24	**TPC A**	**TPC B**	**TPC C**	**TPC D**	**PC 1A**	**PC 6A**	**PC 3A**	**PC 3C**	
		an low	an low	an low	an low					
3	**PC4A** 1–8	**TPC A**	**TPC B**	**TPC C**	**TPC D**	**PC 1B**	**PC 6B**	**PC 3B**	**PC3 D**	
	PC5A 1–8	an high	an high	an high	an high					
2	**s1,s2,s2a**	**TPC A**	**TPC B**	**TPC A**	**TPC B**	**PC 2**	**PC5B** 9–24	**syc bits**	pulser	
	e1,e2	strip low	strip low	strip high	strip high					
1	**clock**	**clock**	**clock**	**clock**	**clock**	**clock**	**clock**	**clock**	**clock**	**clock**

pulser

tdc9/b8 and tdc1/b5 used as pulser tag bit

pulse 100mV DAC gives 40 mV @ disciminator

Tom describe additional overflow bit